

Fig. 3

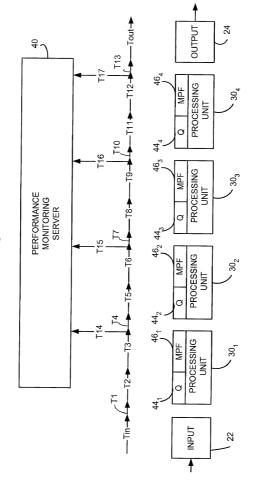
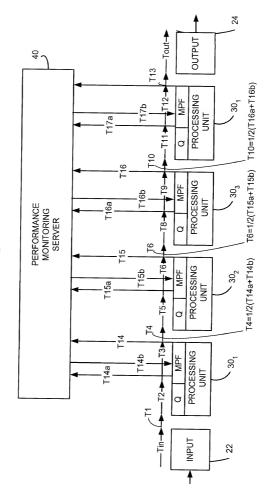
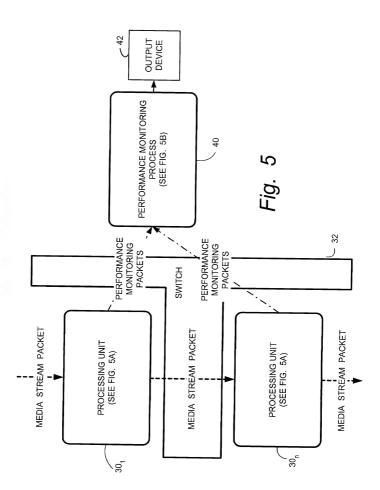
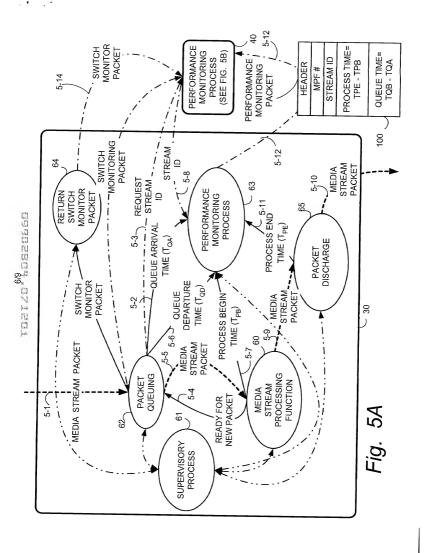


Fig. 4







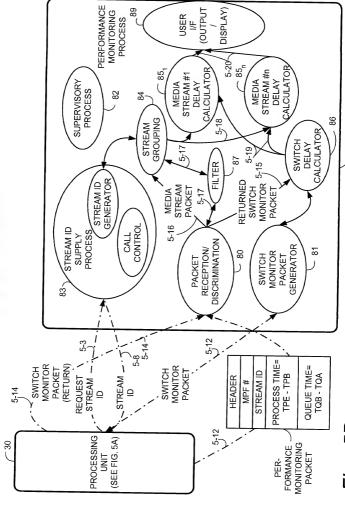


Fig. 5B

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	QD <sub>1-1</sub>	PD <sub>1-1</sub>	TD <sub>1-1</sub>	QD <sub>1-2</sub>	PD <sub>1-2</sub>	TD <sub>1-2</sub>
MEDIA STREAM # 1	QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY	QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY
MED	FIRST PROCESSOR (30 <sub>1</sub> )		SECOND PROCESSOR (30 <sub>2</sub> )			

MEDIA STREAM # q	QD <sub>q-1</sub>	PD <sub>q-1</sub>	TD <sub>q-1</sub>	QD <sub>q-2</sub>	PD <sub>q-2</sub>	TD
	QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY	QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY
	FIRST PROCESSOR (30 <sub>1</sub> )			SECOND PROCESSOR (30 <sub>2</sub> )		

QD <sub>1-4</sub>	PD <sub>1-4</sub>	TD <sub>1-4</sub>	OND
QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY	ODE DELAY
FOURTH	OVERALL NODE DELAY		

:

OND <sub>2</sub>	
OVERALL NODE DELAY	

Fig. 6

Fig. 7

